

Docket No.: P2002,1013

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : MARTIN PERNER
Filed : CONCURRENTLY HEREWITH
Title : CIRCUIT AND METHOD FOR DETERMINING AT LEAST ONE
VOLTAGE, CURRENT AND/OR POWER VALUE FOR AN
INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 6,177,786 B1 (Shimamori), dated January 23, 2001;

U.S. Patent Application Publication No. 2002/0173930 A1 (Perner), dated November 21, 2002, and corresponding German Patent DE 101 14 825 C1 (Perner), dated October 10, 2002;

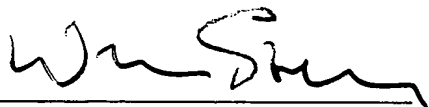
German Published Non-Prosecuted Patent Application DE 101 03 658 A1 (Arndt), dated August 22, 2002 and English abstract thereof;

Tietze, U. et al.: "Halbleiter-Schaltungstechnik" [Electronic Circuits], Monostabile Kippschaltungen, Springer-Verlag, pp. 171-172, 180-181, and 188-191 and English translation: "Electronic Circuits", Springer Verlag, Vol. 9, 1991, pp. 154-155, 162-163, and 170-172;

Hering et al.: "Elektronik für Ingenieure" [Electronics for Engineers], Springer-Verlag, 4th Ed., 2001, pp. 618-621 and 625-626;

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant. As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'W. Stemer', written over a horizontal line.

For Applicant

Date: November 26, 2003 WERNER H. STEMER
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/nt/kf

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: P2002,1013 Appl. No.: <hr/> Applicant: MARTIN PERNER <hr/> Filing Date: November 26, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	6,177,786 B1	1/23/01	Shimamori			
	B	2002/0173930 A1	11/21/02	Perner			
	C						
	D						
	E						
	F						
	G						
	H						
	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	101 14 825 C1	10/10/02	Germany			
	K	101 03 658 A1	8/22/02	Germany			
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Tietze, U. et al.: "Halbleiter-Schaltungstechnik" [Electronic Circuits], Monostabile Kippschaltungen, Springer-Verlag, pp. 171-172, 180-181, and 188-191 and English translation: "Electronic Circuits", Springer Verlag, Vol. 9, 1991, pp. 154-155, 162-163, and 170-172					
		Hering et al.: "Elektronik für Ingenieure" [Electronics for Engineers], Springer-Verlag, 4 th Ed., 2001, pp. 618-621 and 625-626					
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							